

Fig. 2

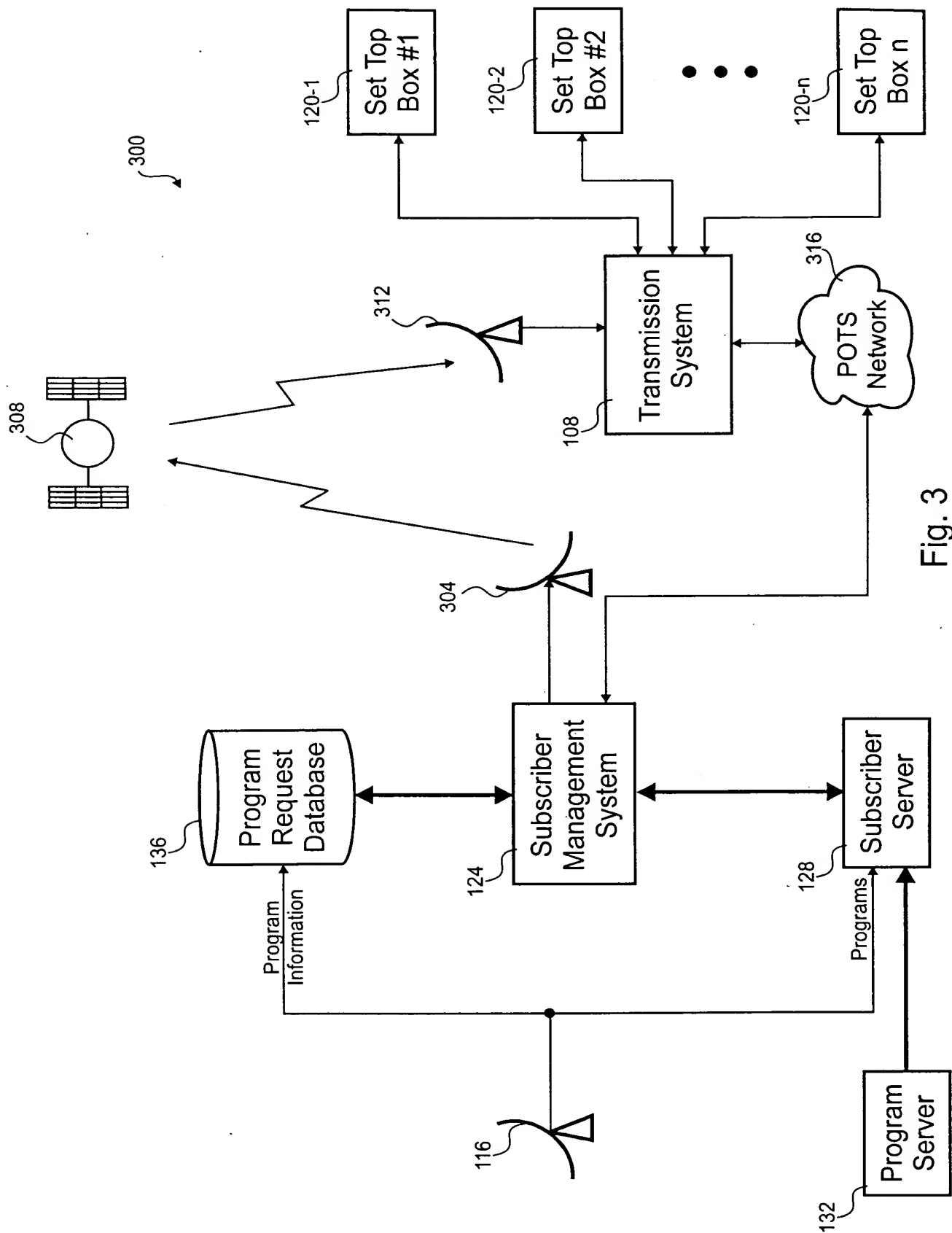


Fig. 3

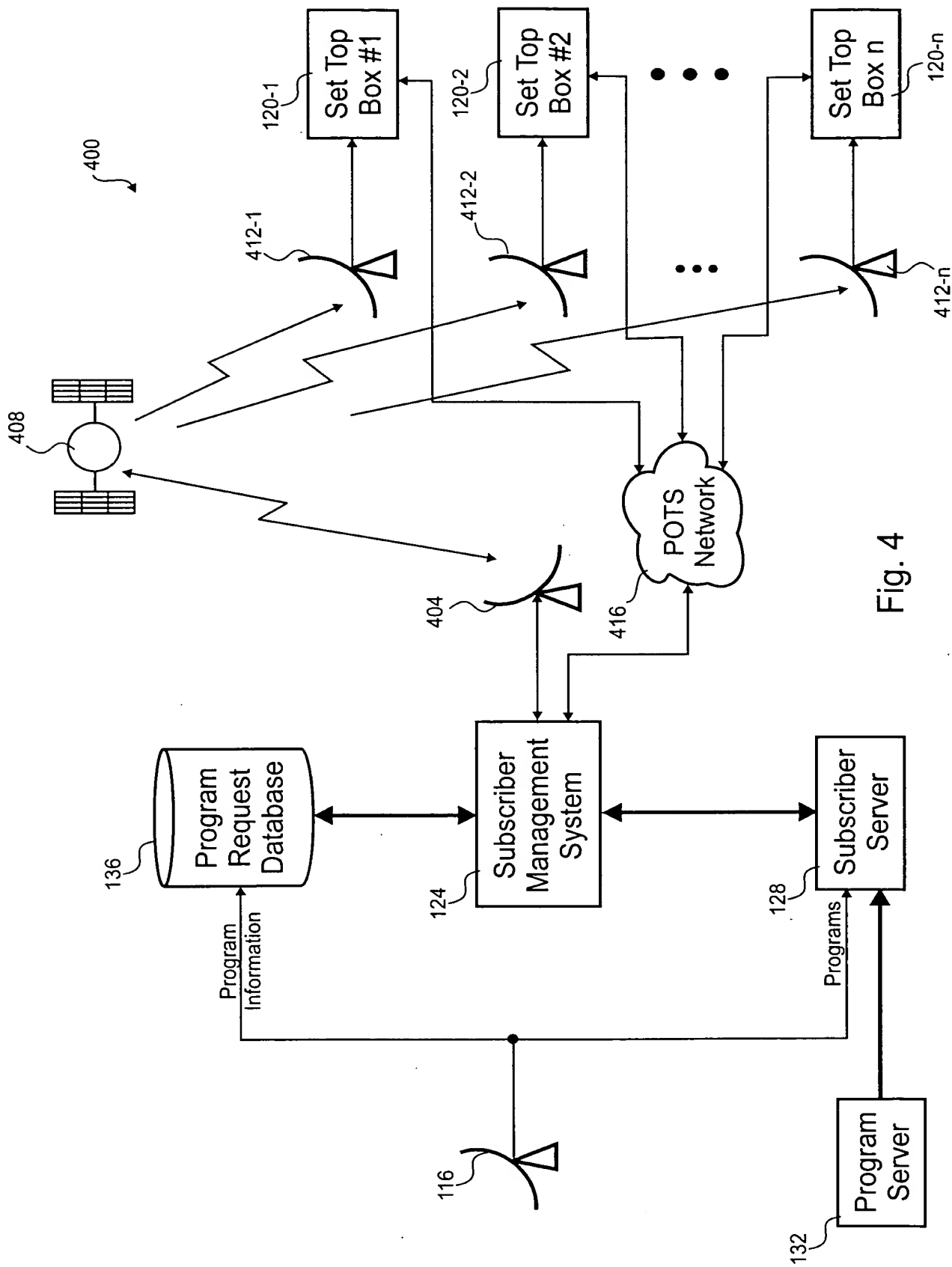


Fig. 4

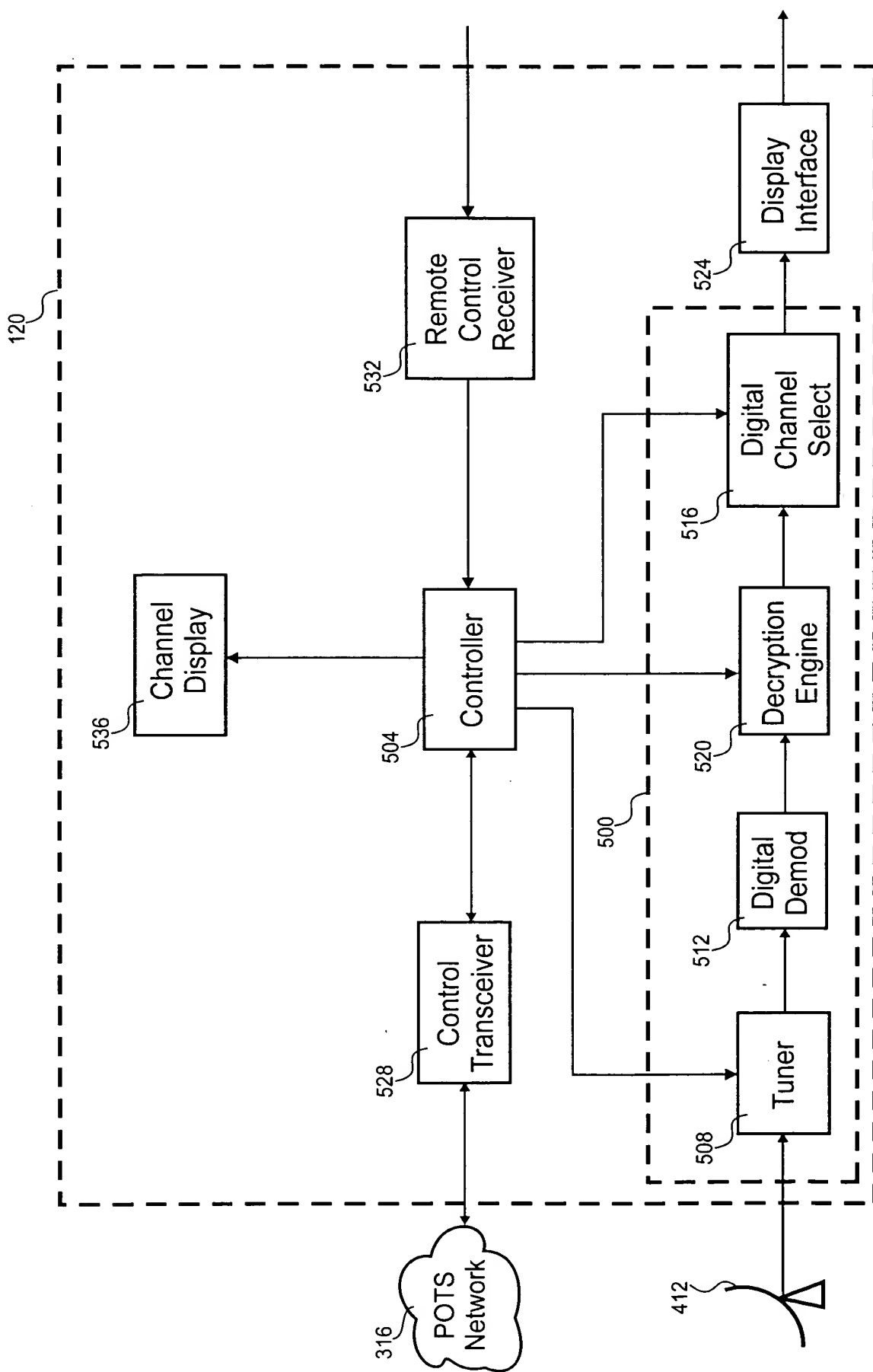


Fig. 5

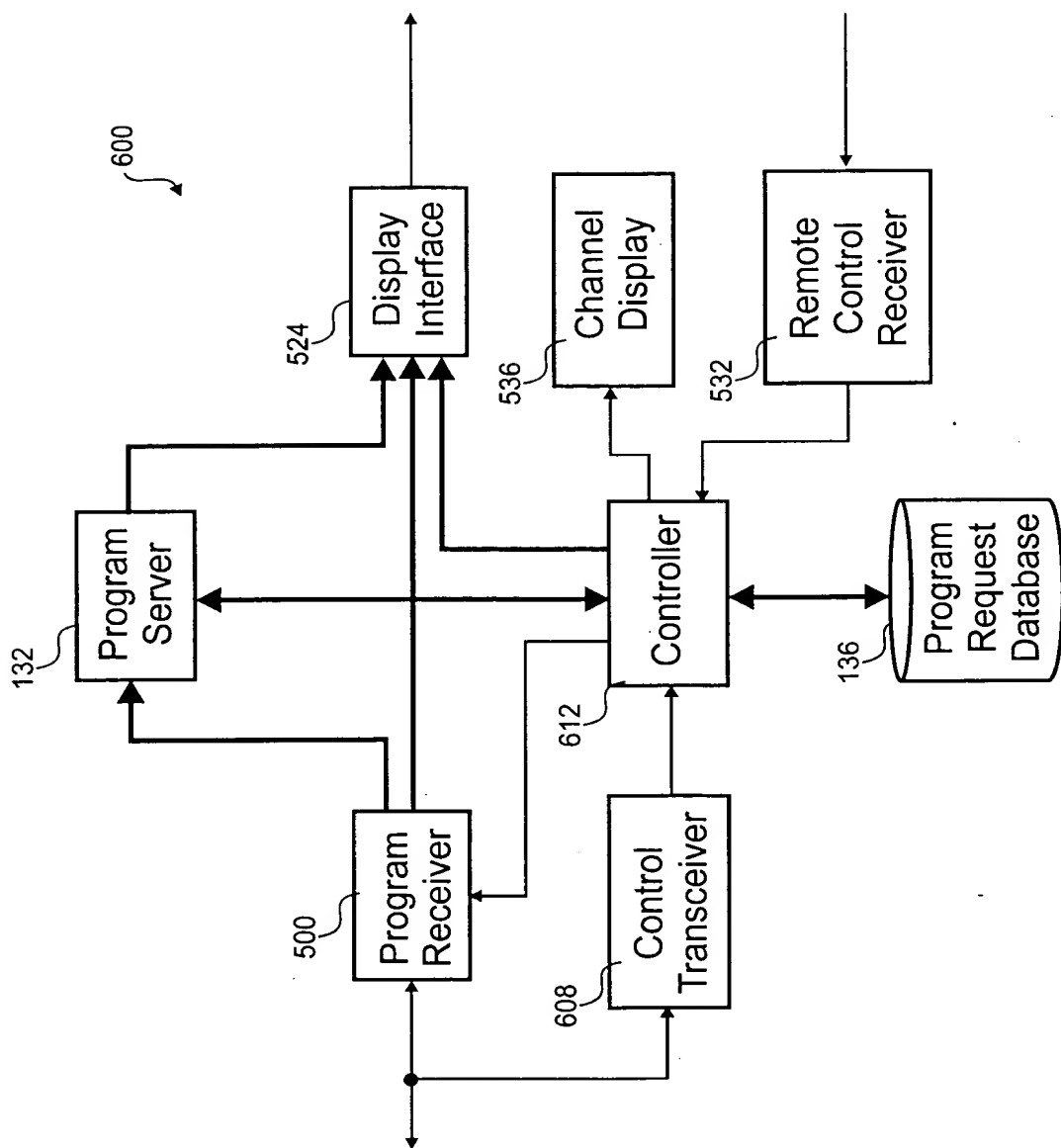


Fig. 6

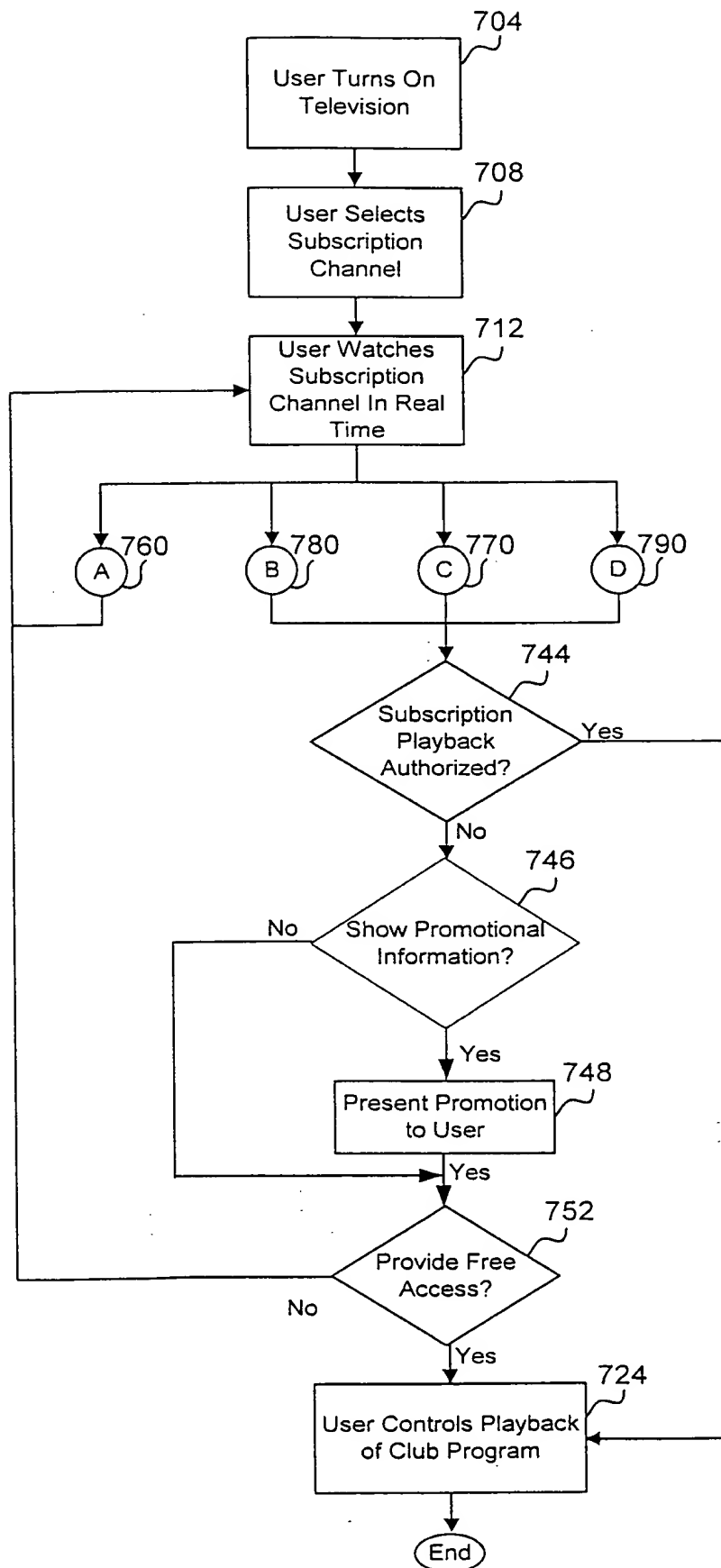


FIG. 7

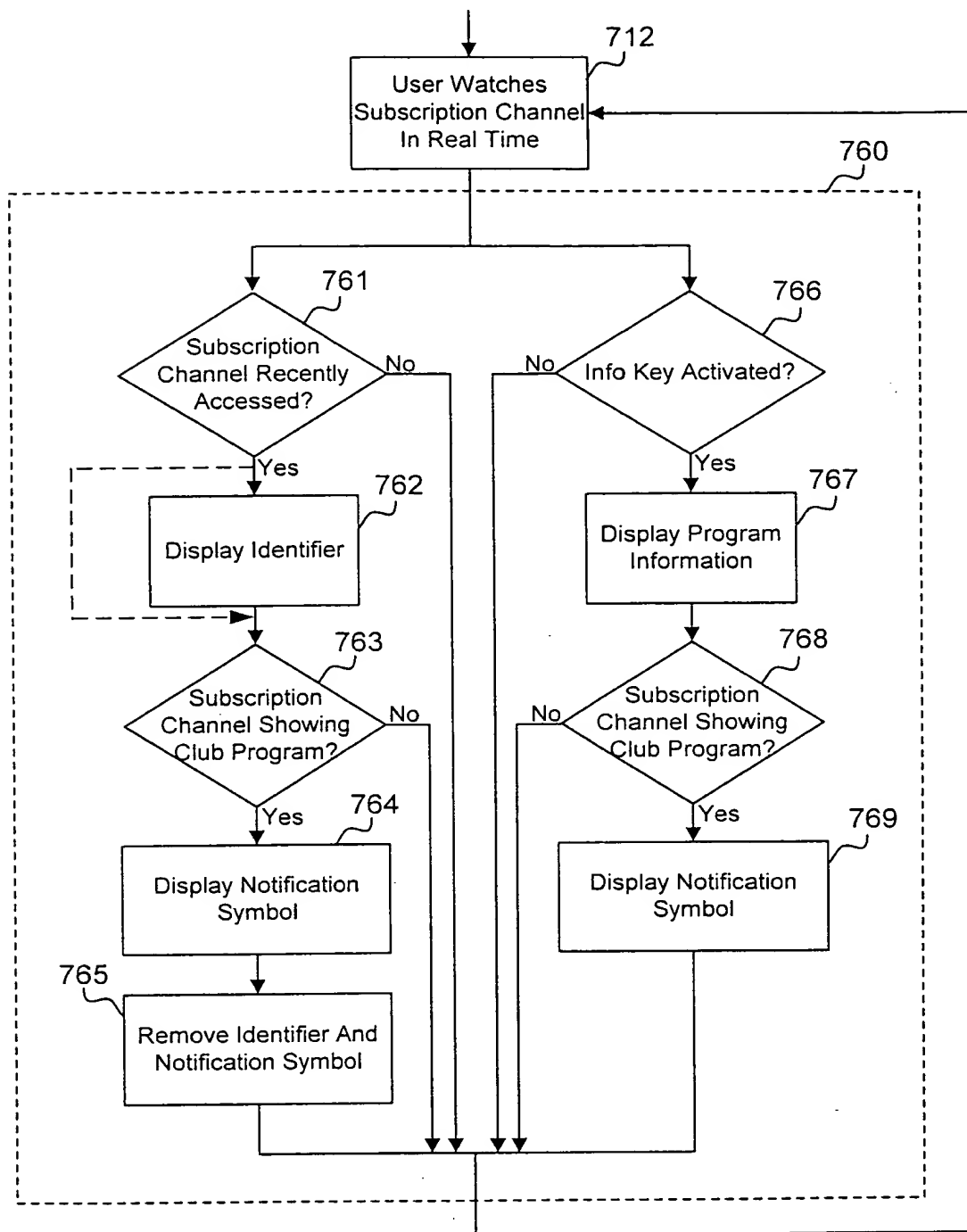


FIG. 7A

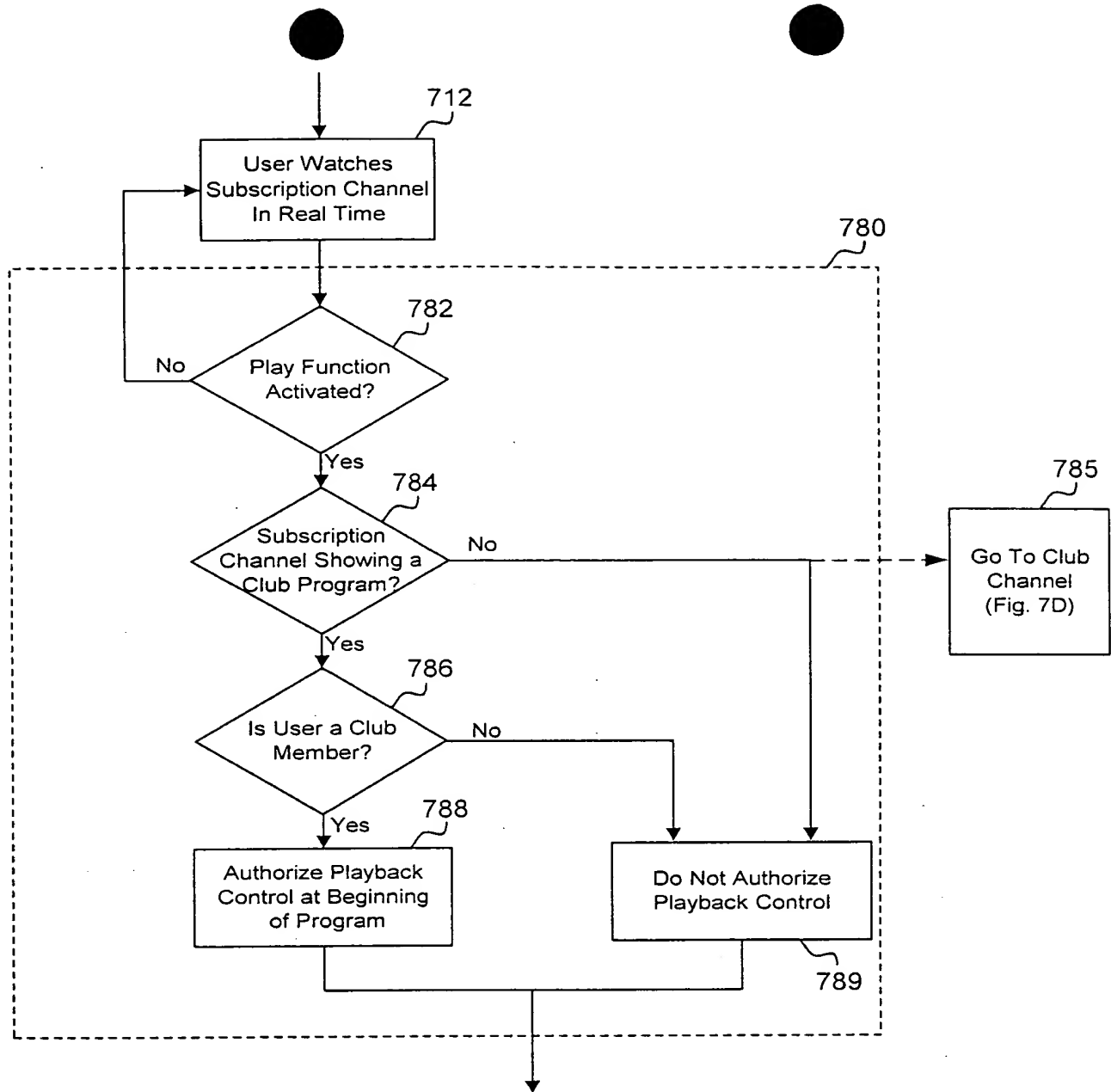


FIG. 7B

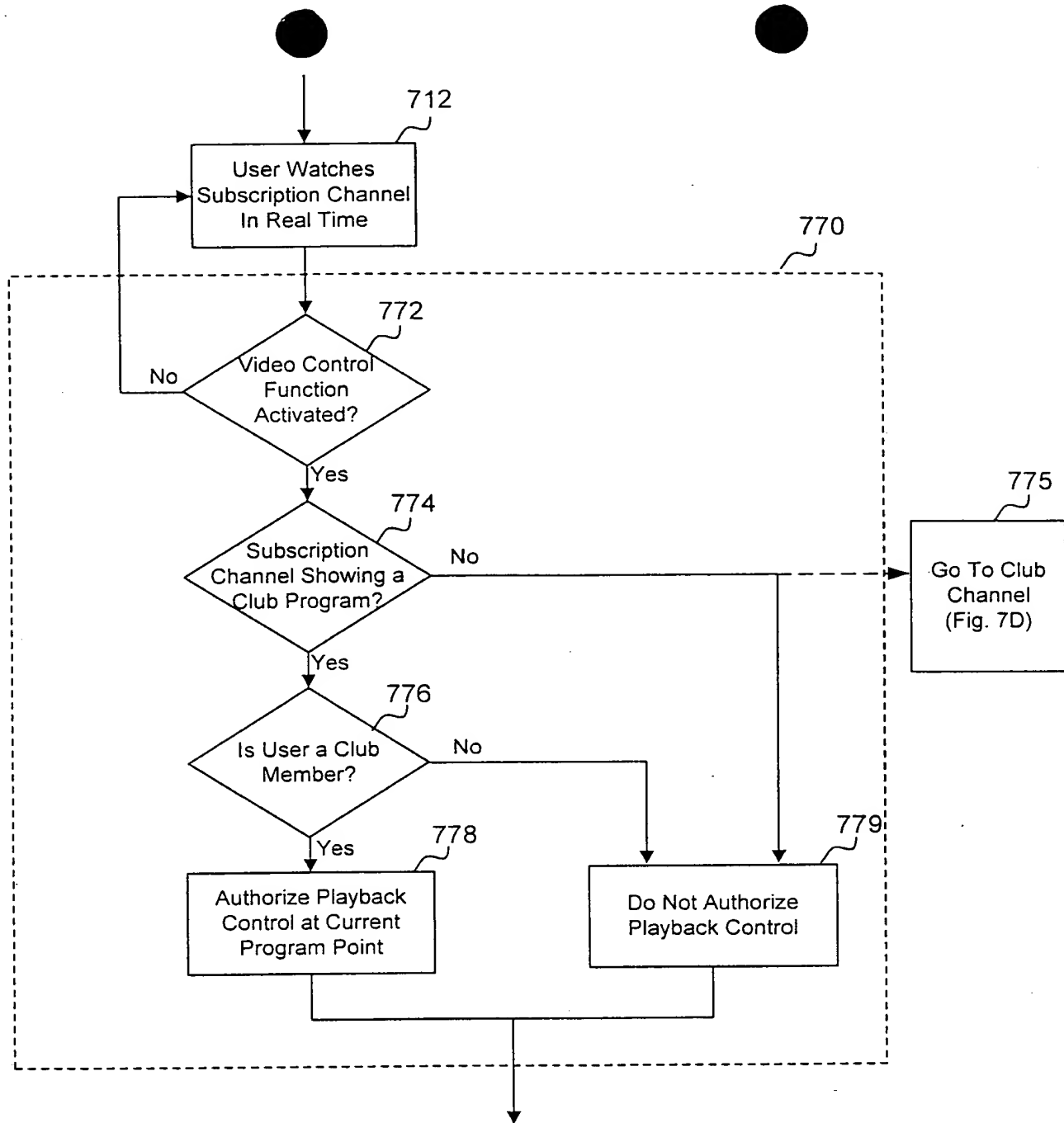


FIG. 7C

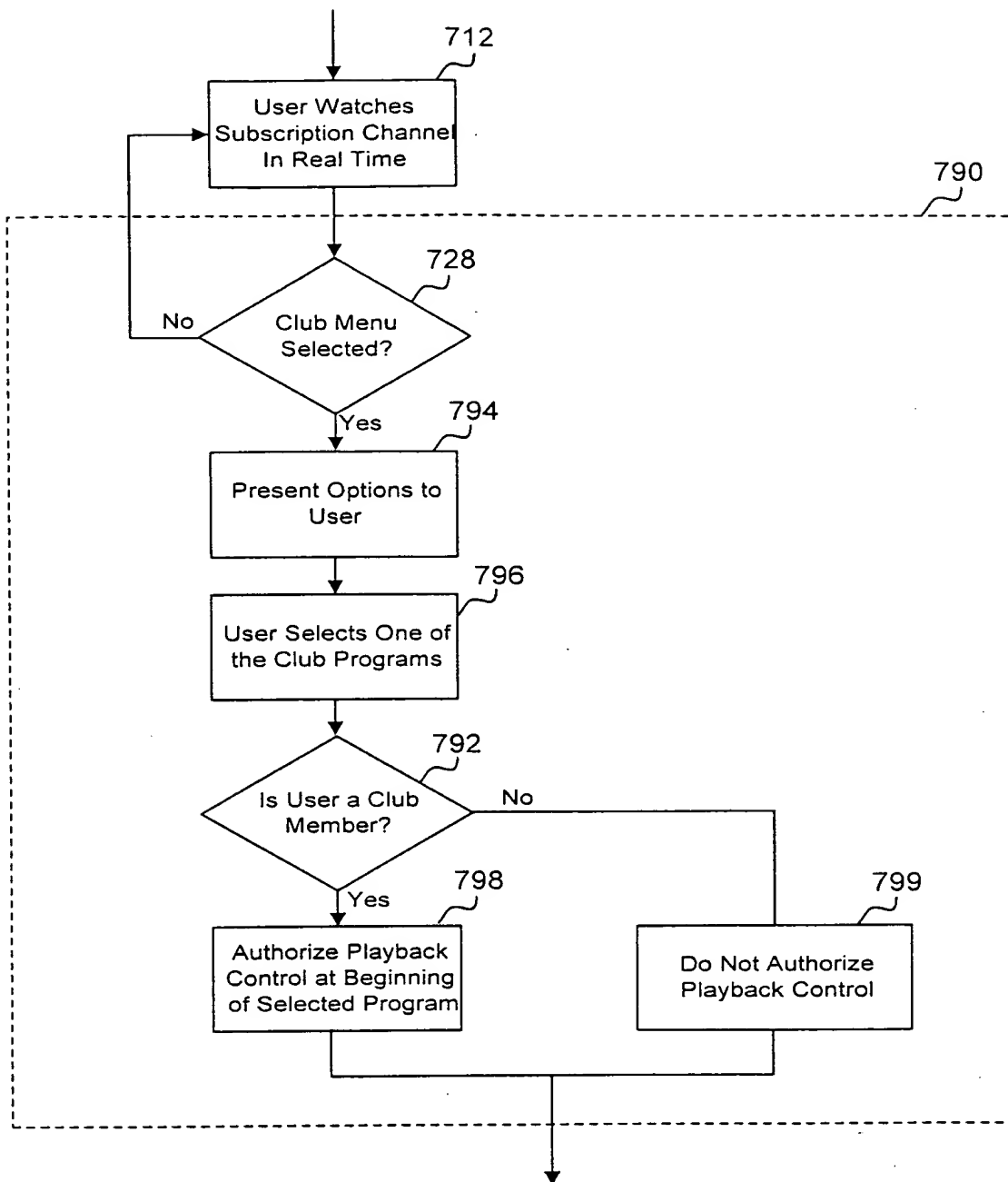


FIG. 7D

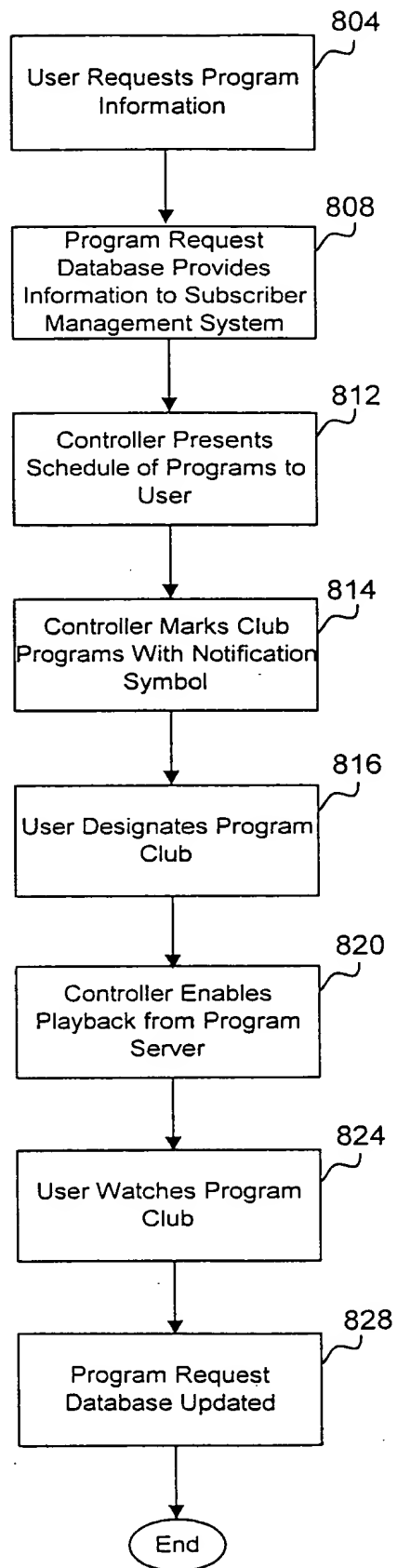


FIG. 8A

	6:00	6:30	7:00	7:30	8:00	8:30	9:00	9:30	10:00	10:30	11:00
Ch. A	840-1		840-2	☆	842	840-3	☆	840-1	840-4	☆	842
Ch. B		840-5		842	840-4		842	840-6	840-1	☆	840-1
Ch. C		840-7	☆		840-7	☆	840-8	840-9			
Ch. D	840-8			840-10				840-10	842	☆	
Ch. E		840-11	842		840-4	842	840-3	840-8			
Ch. F		840-3	☆	840-6	840-10	☆	840-10	840-7	☆		
Ch. G		840-12									

FIG. 8B

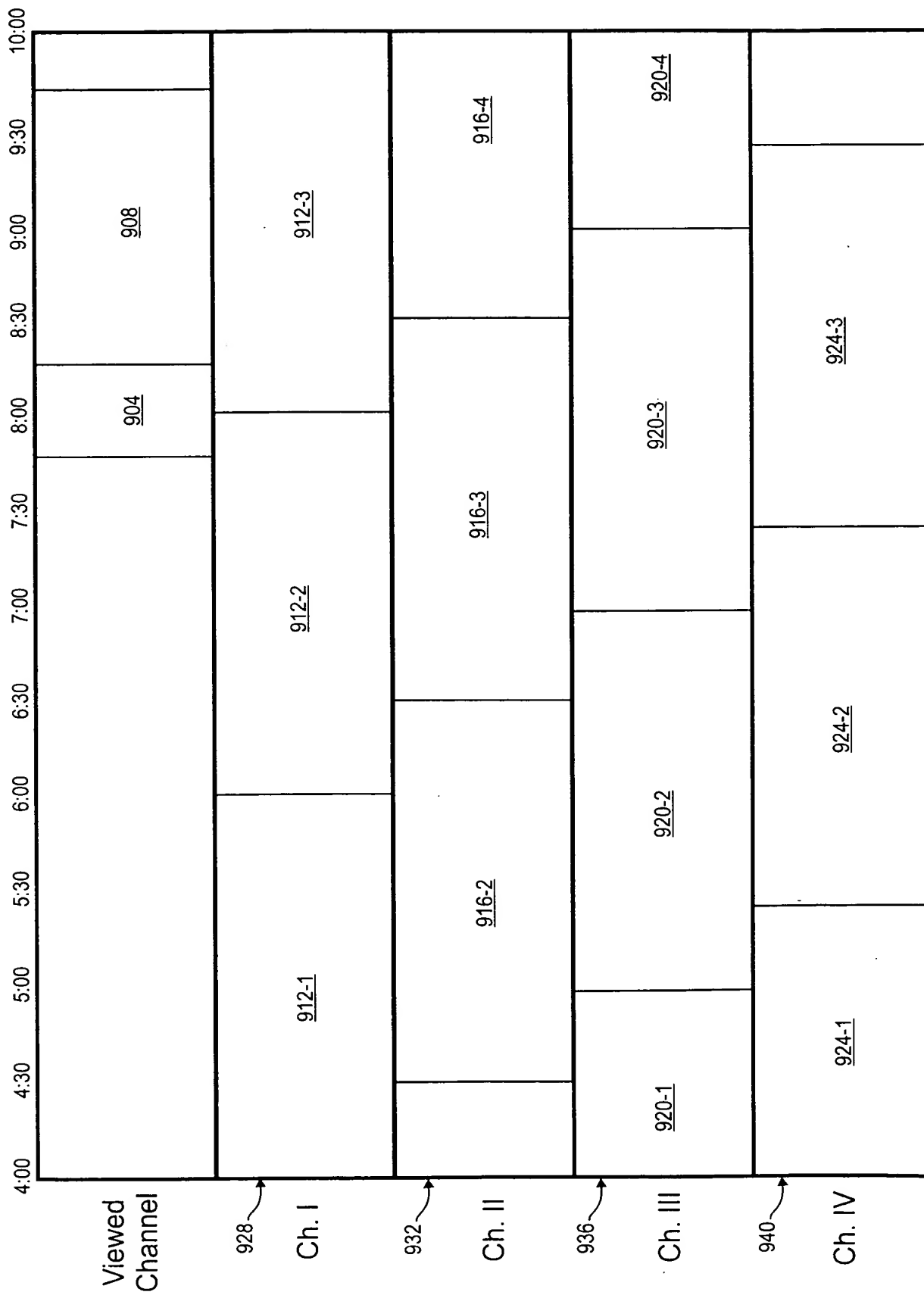


Fig. 9A

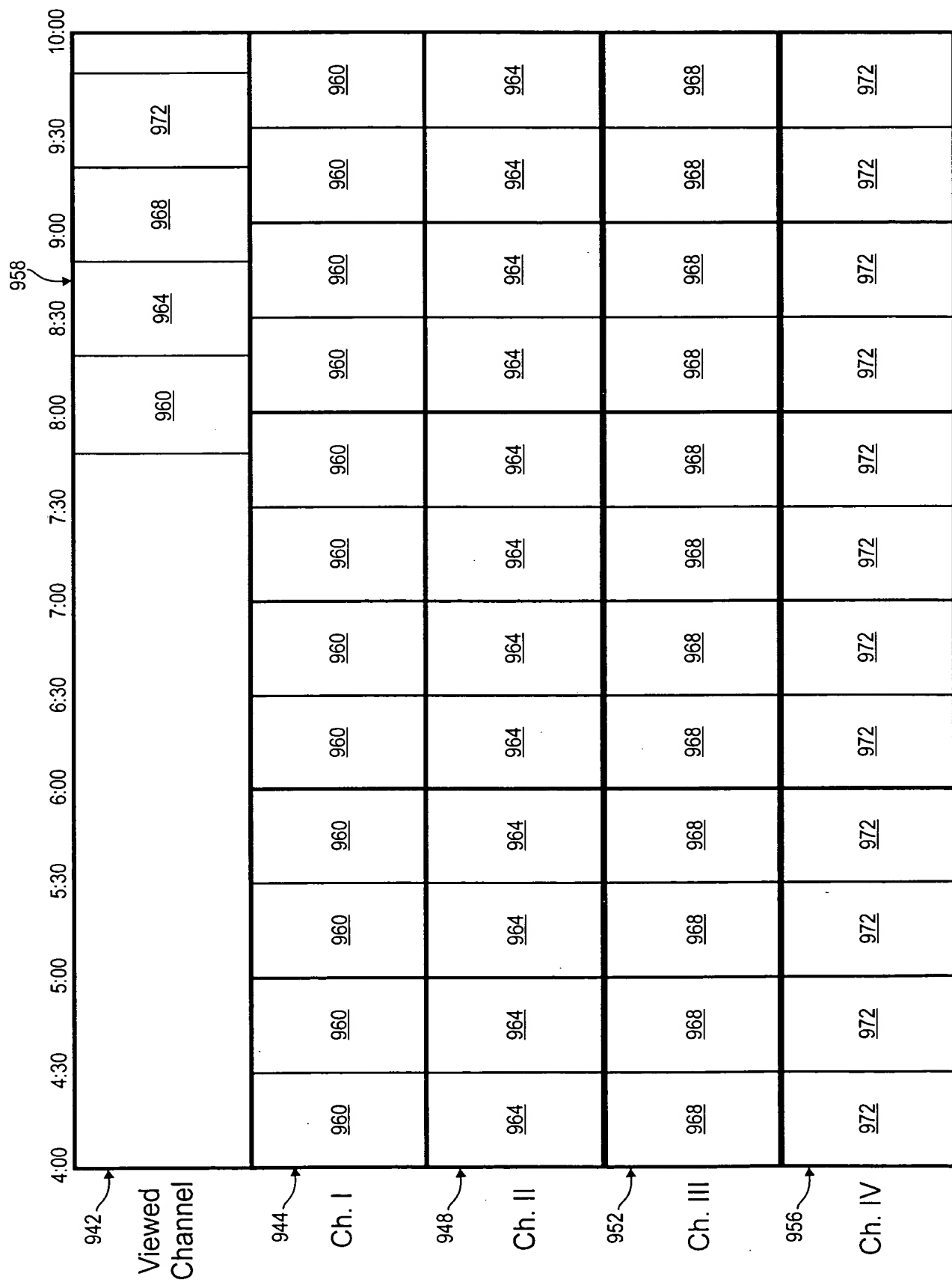


Fig. 9B

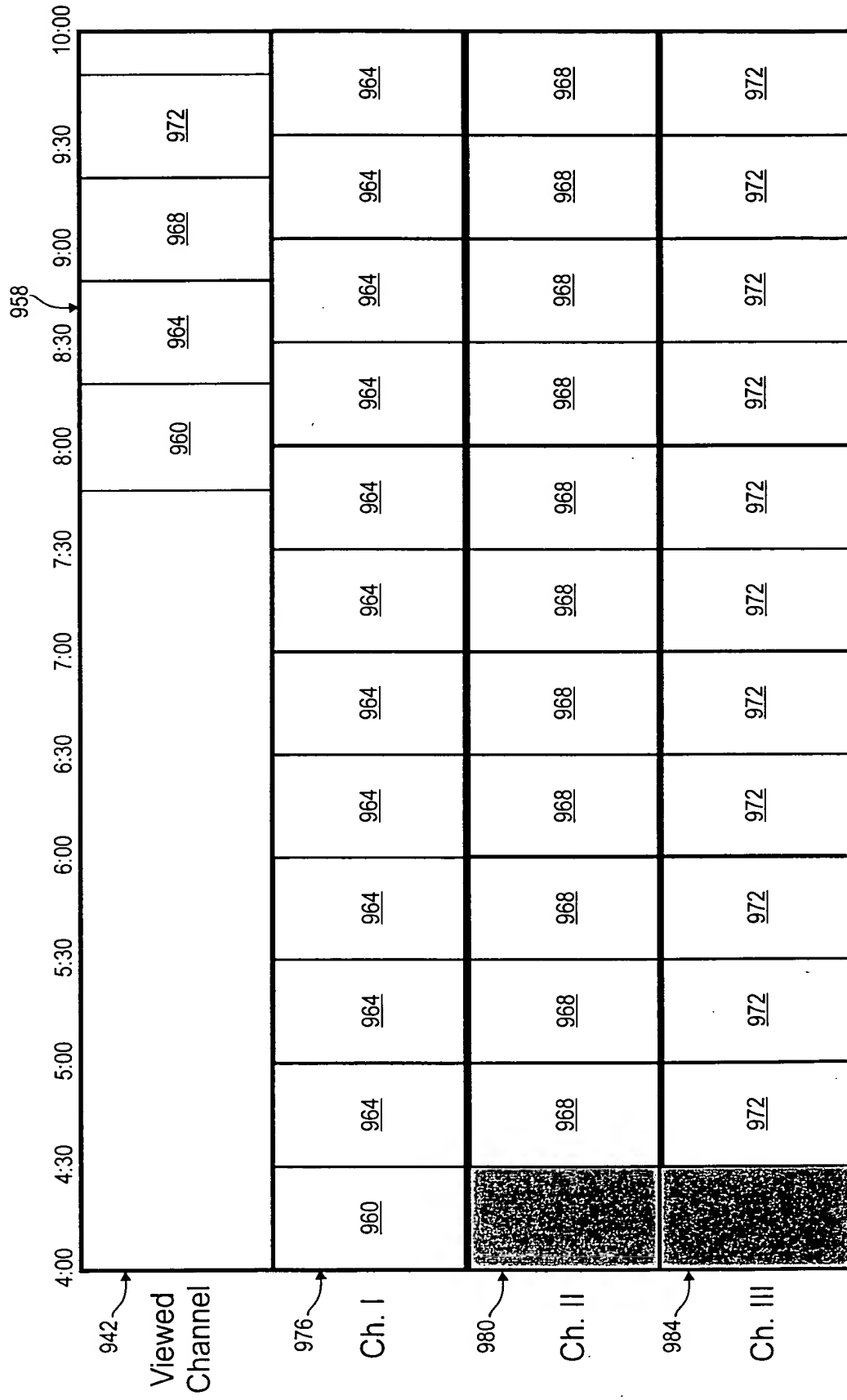


Fig. 9C

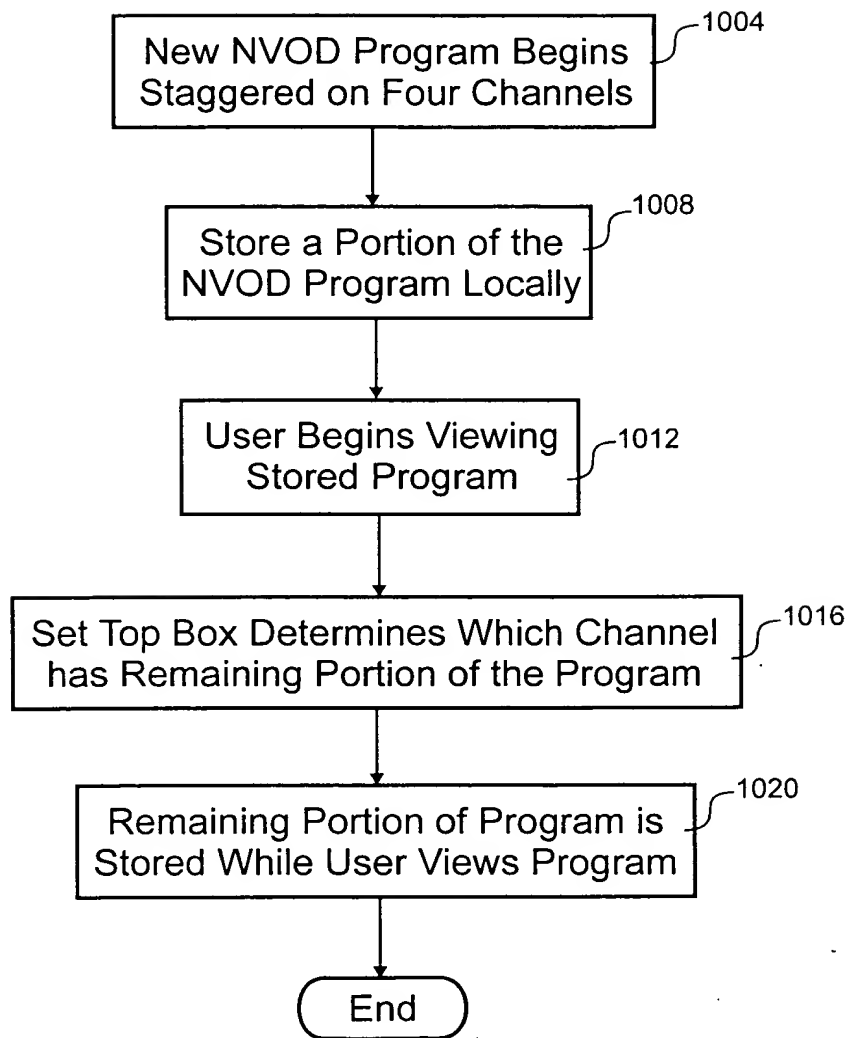


Fig. 10A

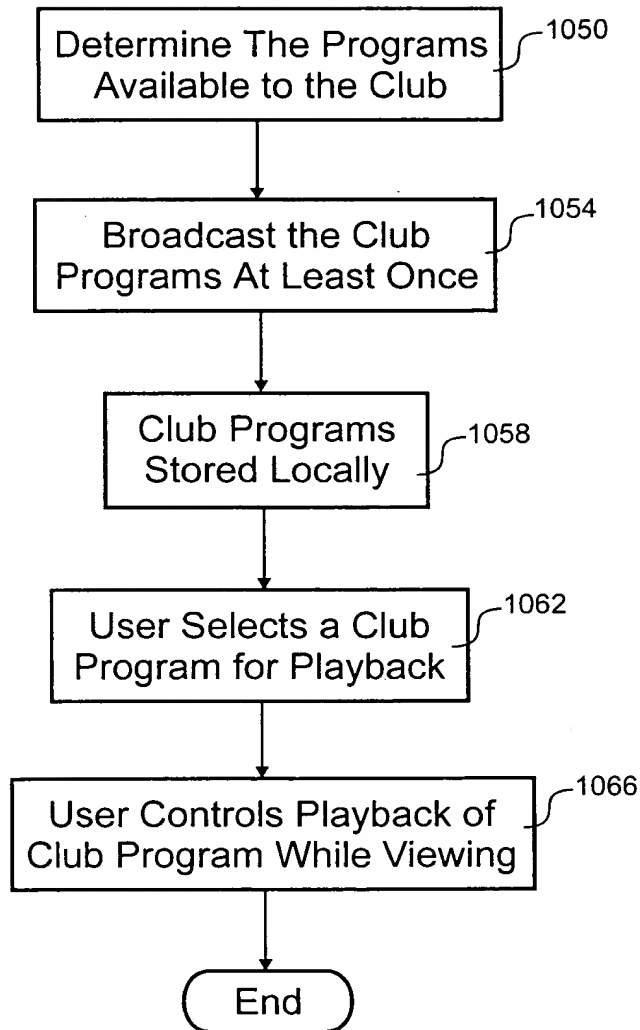


Fig. 10B

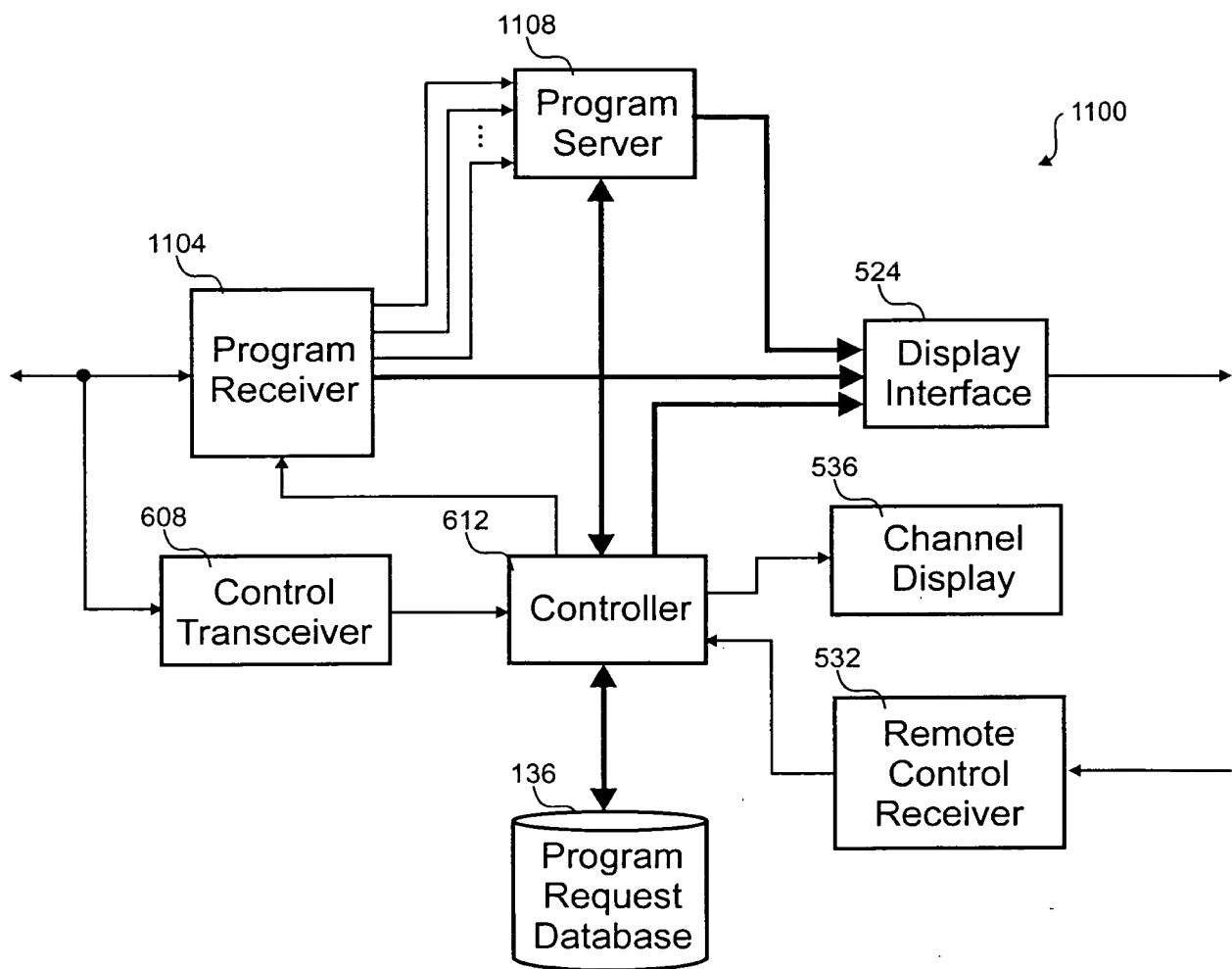


Fig. 11

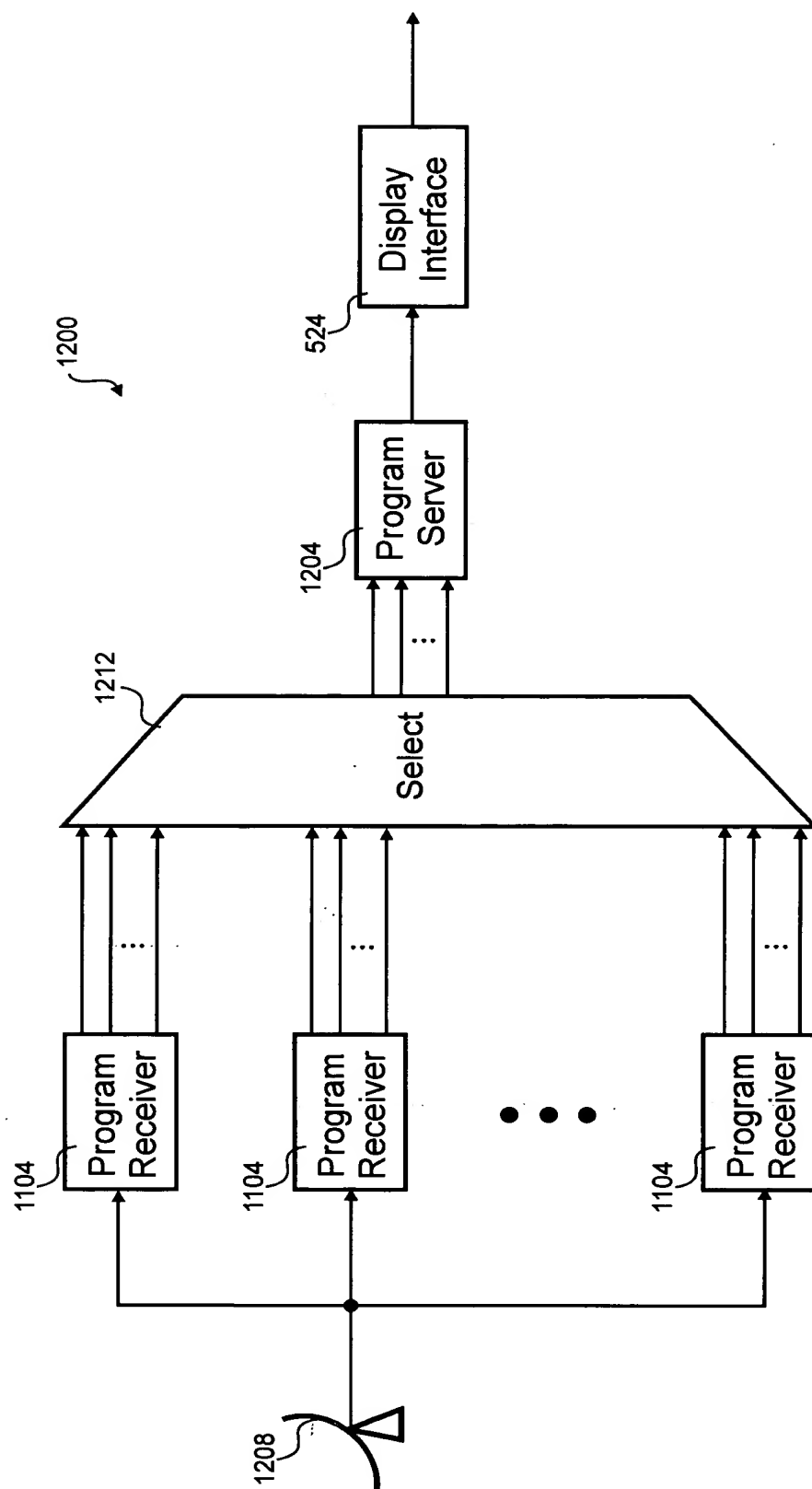


Fig. 12A

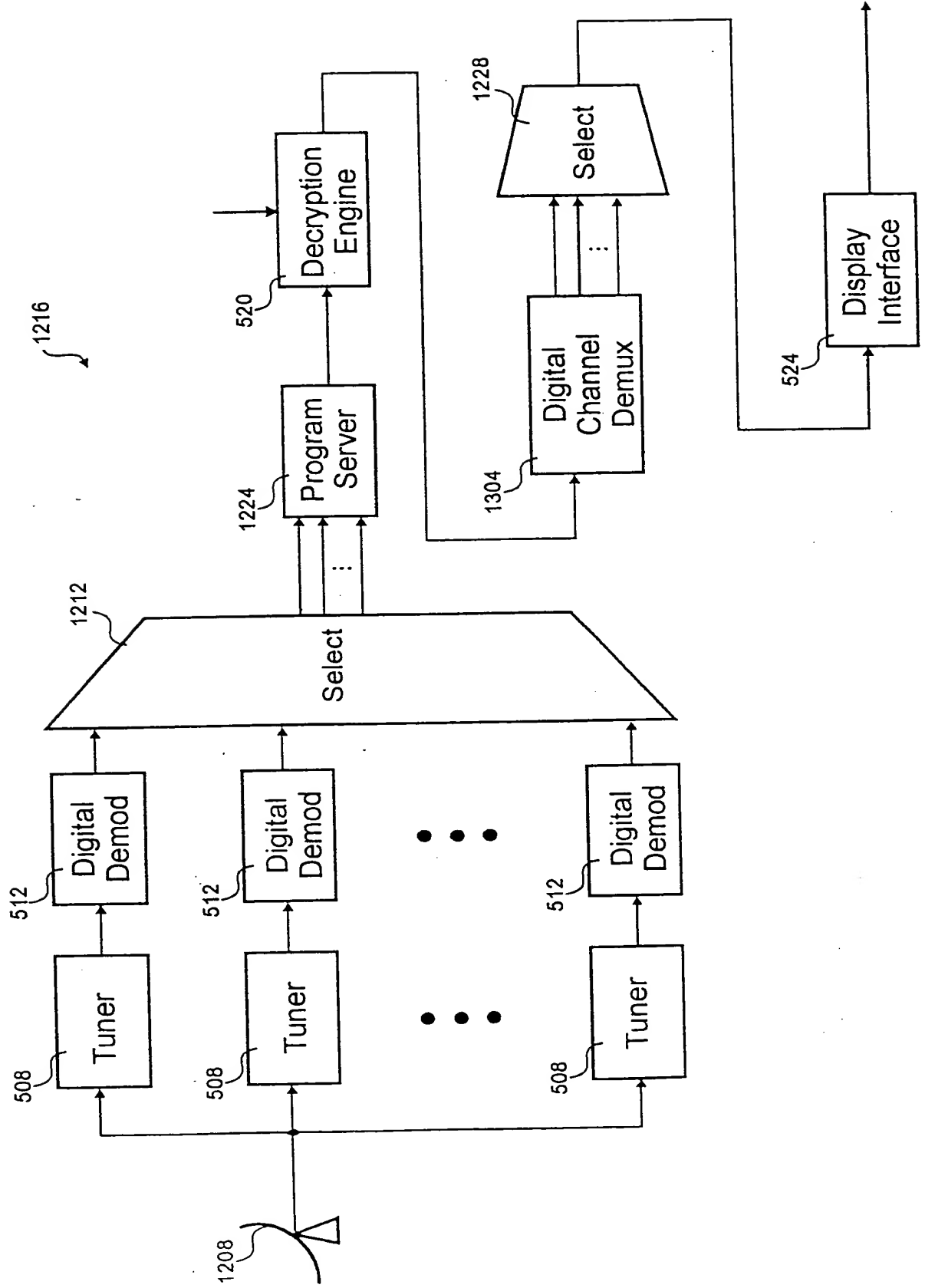


Fig. 12B

1104

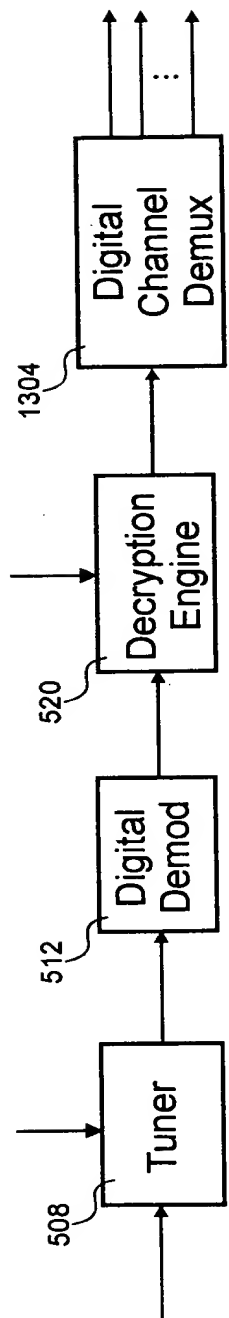


Fig. 13A

FIG. 13B is a block diagram of a digital receiver system 1104. The system includes a Tuner 508, a Digital Demod 512, a Digital Channel Demux 1304, a Select 1228, and a Decryption Engine 520. The Tuner 508 receives an input signal and outputs to the Digital Demod 512. The Digital Demod 512 outputs to the Digital Channel Demux 1304. The Digital Channel Demux 1304 outputs multiple signals to the Select 1228. The Select 1228 outputs to the Decryption Engine 520. The Decryption Engine 520 outputs the final signal.

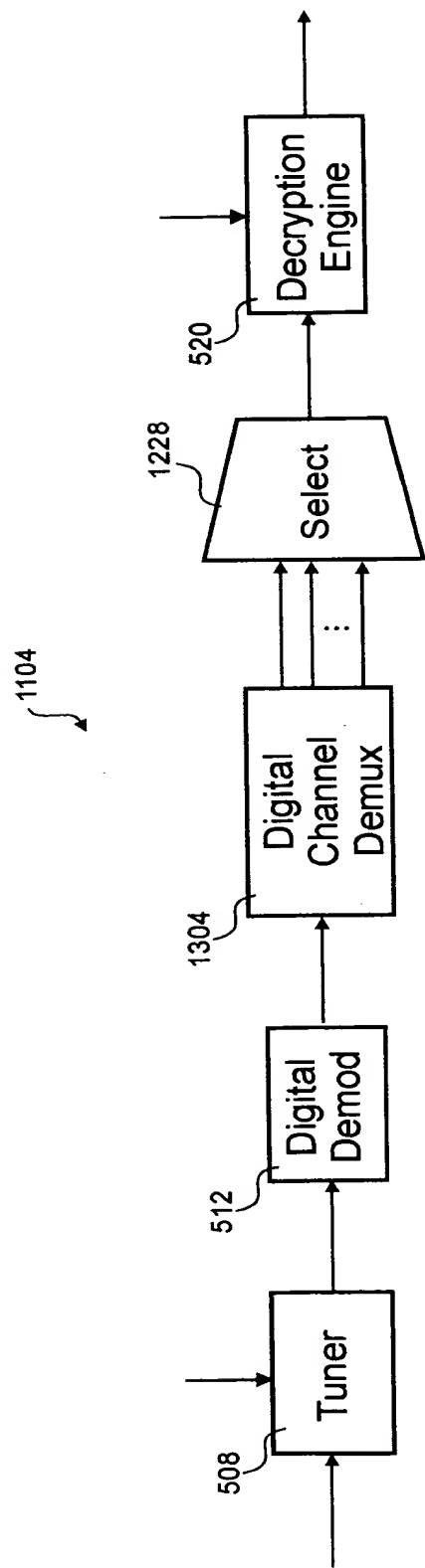


Fig. 13B

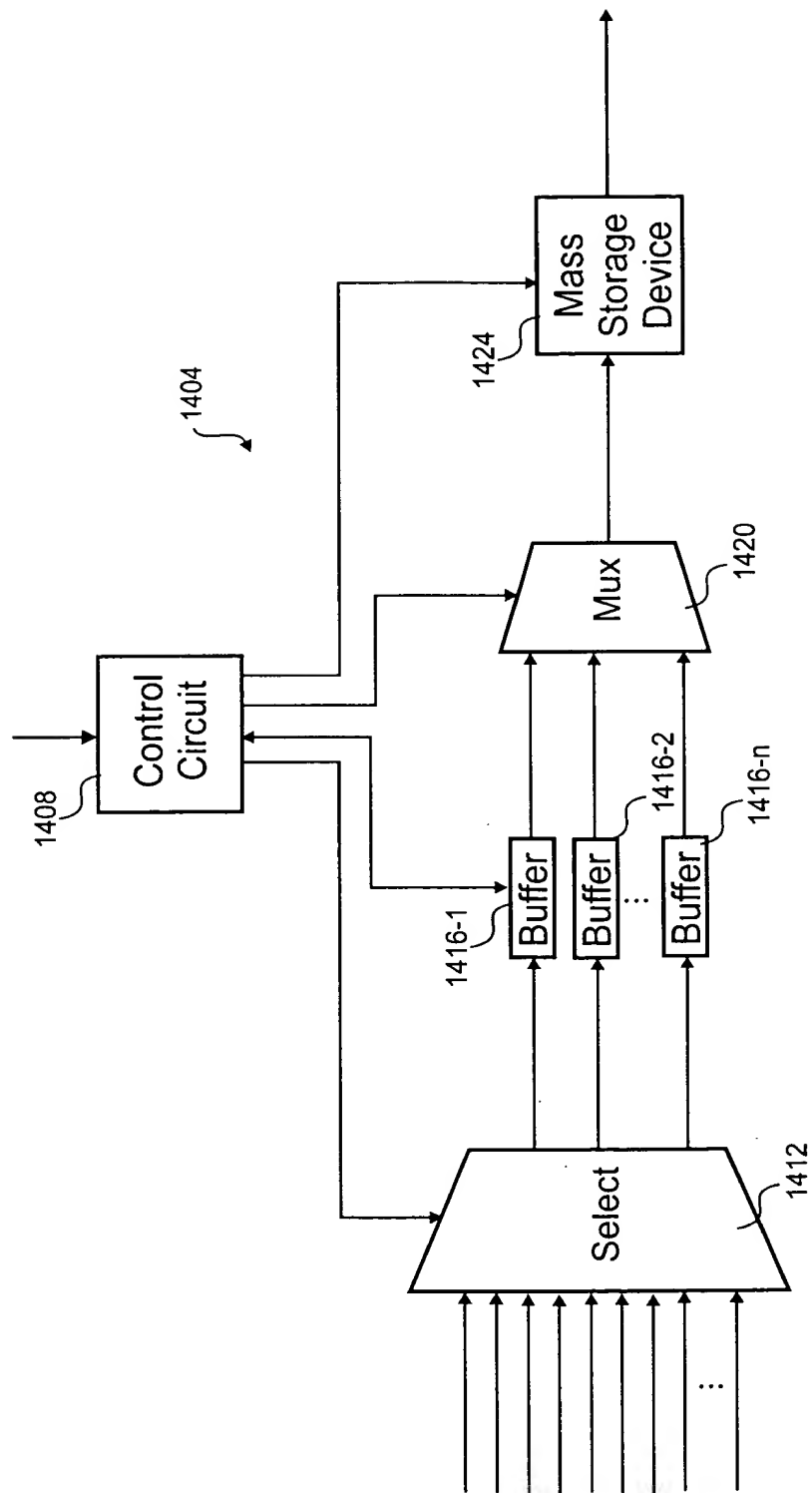


Fig. 14

FIG. 15 is a block diagram of a system 1504, according to one embodiment of the present invention. The system 1504 includes a control circuit 1508, a select device 1512, a multiplexer (Mux) 1516, a mass storage device 1520, and a select device 1524. The control circuit 1508 is connected to the select device 1512, the Mux 1516, the mass storage device 1520, and the select device 1524. The select device 1512 receives multiple inputs (indicated by vertical arrows) and outputs to the Mux 1516. The Mux 1516 outputs to the mass storage device 1520. The mass storage device 1520 outputs to the select device 1524, which then outputs to an external output (indicated by a horizontal arrow).

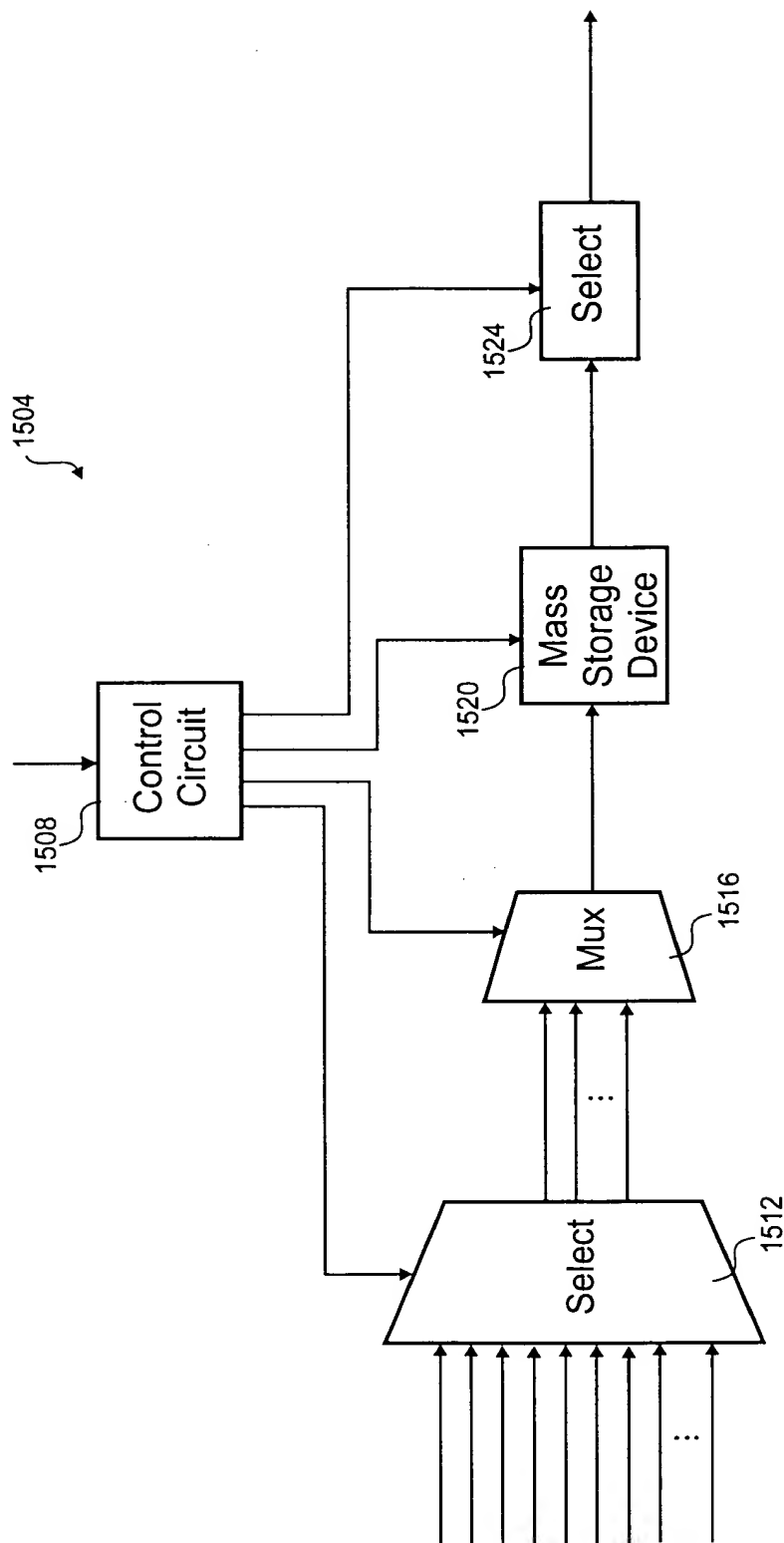


Fig. 15

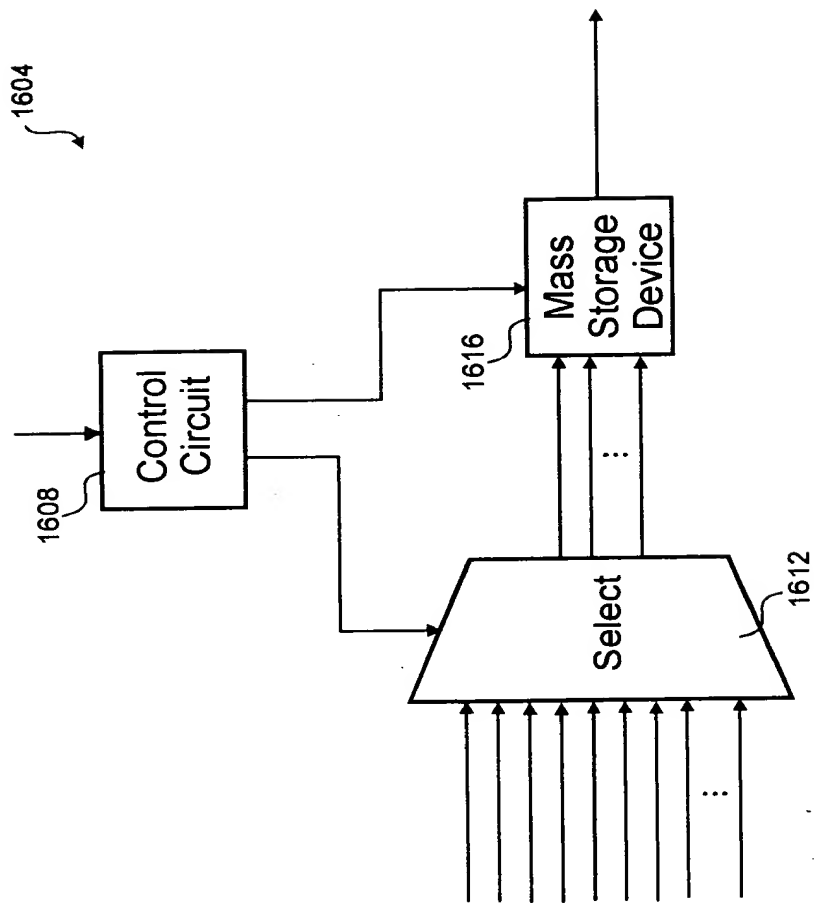


Fig. 16